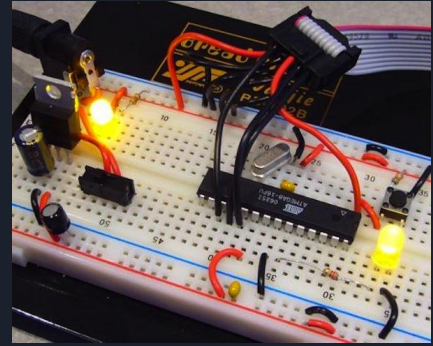# VirtualLogicLabs

By:
Group #10

Sagar Phanda, Khalid Akash, Yiwen Tao, Joseph Cella, Dhruvik Patel, Vikas Khan
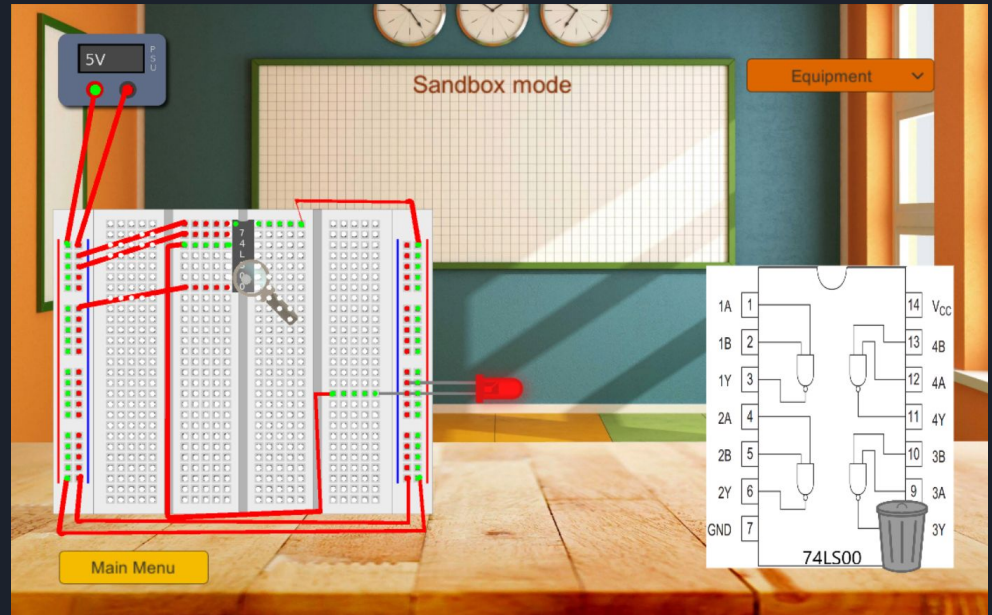
Background

- Traditional Digital Logic Design Lab Issues:
  - Assisting Students
  - Grading Work
  - Defective Parts
  - Cost of Assistants, and Devices
- How to fix these issues
  - Create a tool to help complete labs online - cheap and accessible to everybody
  - Provide a grading system
  - Allow instructors to communicate with students

Current Features:

- Accurate simulation of Digital Logic circuits

- A functional Digital Logic Design Lab for students to complete

- Pre and post tests to ensure that students understand the concepts

Upcoming features

- Fully online integration with a **remote database** for the following features
    - A **grading** system for instructors to keep track of students' progress
    - A **cheat detection system** to make sure students are completing tasks honestly
    - A **chat/messaging** for students to interact with instructors and each other
    - A **feedback system** to help the students complete the lab

Live Demo