14:332:231 DIGITAL LOGIC DESIGN

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Lecture #19: Designing State Machines Using State Diagrams



























	Transition List													
	•	Simila speci	ar to fied	b a by	trar exp	<i>sition table</i> , but tra pressions, not by a	ansitio n ext	ons ir ensiv	the s tab	state ulatic	diagram are on of next states			
		Current State						Next	State					
					$\overline{}$									
		S	Q2	Q1	Q0	Transition Expression	S*	Q2*	Q1+	Q0+				
	ſ	IDLE	0	0	0	(LEFT + RIGHT + HAZ)'	IDLE	0	0	0				
	J	IDLE	0	0	0	LEFT · HAZ' · RIGHT'	L1	0	0	1				
)	IDLE	0	0	0	HAZ + LEFT · RIGHT	LR3	1	0	0				
S	U	IDLE	0	0	0	RIGHT · HAZ' · LEFT'	R1	1	0	1				
ion	5	L1	0	0	1	HAZ'	L2	0	1	1				
Jsit	J	L1	0	0	1	HAZ	LR3	1	0	0	From a transition			
rar	5	L2	0	1	1	HAZ'	L3	0	1	0	list, circuit synthesis			
g T	l	L2	0	1	1	HAZ	LR3	1	0	0	is just "turning-the-			
oi	Ę,	L3	0	1	0	1	IDLE	0	0	0	crank," —			
Itg	Ş	R1	1	0	1	HAZ'	R2	1	1	1	automated using a			
ō	L	R1	1	0	1	HAZ	LR3	1	0	0	CAD tool			
	5	R2	1	1	1	HAZ'	R3	1	1	0				
	ો	R2	1	1	1	HAZ	LR3	1	0	0				
	E	R3	1	1	0	1	IDLE	0	0	0				
	C	LR3	1	0	0	1	IDLE	0	0	0	15 of 22			









Tro	ar	١S	it	ion List fo	or G	Gu	es	sii	ng	6	20	m	ie
Curr	rent	Stat	e			Output							
s	Q2	Q1	Q0	Transition Expression	S*	Q2*	Q1*	Q0*	L1	L2	L3	L4	ER
S 1	0	0	0	G1'·G2'·G3'·G4'	S2	0	0	1	1	0	0	0	0
S1	0	0	0	G1·G2'·G3'·G4'	SOK	1	0	0	1	0	0	0	0
S1	0	0	0	G2+G3+G4	SERR	1	0	1	1	0	0	0	0
S 2	0	0	1	G1'·G2'·G3'·G4'	S3	0	1	1	0	1	0	0	0
S2	0	0	1	G1'·G2·G3'·G4'	SOK	1	0	0	0	1	0	0	0
S2	0	0	1	G1+G3+G4	SERR	1	0	1	0	1	0	0	0
S 3	0	1	1	G1'·G2'·G3'·G4'	S4	0	1	0	0	0	1	0	0
S3	0	1	1	G1'·G2'·G3·G4'	SOK	1	0	0	0	0	1	0	0
S3	0	1	1	G1+G2+G4	SERR	1	0	1	0	0	1	0	0
S 4	0	1	0	G1'·G2'·G3'·G4'	S1	0	0	0	0	0	0	1	0
🖌 S4	0	1	0	G1'·G2'·G3'·G4	SOK	1	0	0	0	0	0	1	0
S4	0	1	0	G1+G2+G3	SERR	1	0	1	0	0	0	1	0
🖌 SOK	1	0	0	G1+G2+G3+G4	SOK	1	0	0	0	0	0	0	0
€ sok	1	0	0	G1'·G2'·G3'·G4'	S1	0	0	0	0	0	0	0	0
∫ SERR	1	0	1	G1+G2+G3+G4	SERR	1	0	1	0	0	0	0	1
	1	0	1	G1':G2':G3':G4'	S1	0	0	0	0	0	0	0	1



